

(2) Values of Variables:

- Extend the set  $Expr$  of expressions by occurring loads  $M[e]$ .
- Extend the Effects of Edges:

$$\begin{aligned} \llbracket x = M[e]; \rrbracket^\# V e' &= \begin{cases} \{x\} & \text{if } e' = M[e] \\ \emptyset & \text{if } e' = e \\ V e' \setminus \{x\} & \text{otherwise} \end{cases} \\ \llbracket M[e_1] = e_2; \rrbracket^\# V e' &= \begin{cases} \emptyset & \text{if } e' \in \{e_1, e_2\} \\ V e' & \text{otherwise} \end{cases} \end{aligned}$$

### (3) Constant Propagation:

- Extend the abstract state by an abstract store  $M$
- Execute accesses to known memory locations!

$$\begin{aligned}
 \llbracket x = M[e]; \rrbracket^\# (D, M) &= \begin{cases} (D \oplus \{x \mapsto M a\}, M) & \text{if} \\ & \llbracket e \rrbracket^\# D = a \sqsubset \top \\ (D \oplus \{x \mapsto \top\}, M) & \text{otherwise} \end{cases} \\
 \llbracket M[e_1] = e_2; \rrbracket^\# (D, M) &= \begin{cases} (D, M \oplus \{a \mapsto \llbracket e_2 \rrbracket^\# D\}) & \text{if} \\ & \llbracket e_1 \rrbracket^\# D = a \sqsubset \top \\ (D, \underline{\top}) & \text{otherwise} \end{cases} \quad \text{where} \\
 \underline{\top} a &= \top \quad (a \in \mathbb{N})
 \end{aligned}$$

## Problems:

- Addresses are from  $\mathbb{N}$  :-(  
There are **no infinite** strictly ascending chains, but ...
- Exact addresses at compile-time are **rarely** known :-(  
At the same program point, typically different addresses are accessed ...
- Storing at an **unknown** address destroys all information  $\mathbb{M}$  :-(  
At the same program point, typically different addresses are accessed ...

⇒ constant propagation fails :-(  
At the same program point, typically different addresses are accessed ...

⇒ memory accesses/pointers **kill precision** :-(  
At the same program point, typically different addresses are accessed ...

## Simplification:

- We consider pointers to the beginning of **blocks**  $A$  which allow indexed accesses  $A[i]$  :-)
- We ignore well-typedness of the blocks.
- New statements:

$x = \text{new}();$  // allocation of a new block

$x = y[e];$  // indexed read access to a block

$y[e_1] = e_2;$  // indexed write access to a block

- Blocks are possibly infinite :-)
- For simplicity, all pointers point to the beginning of a block.

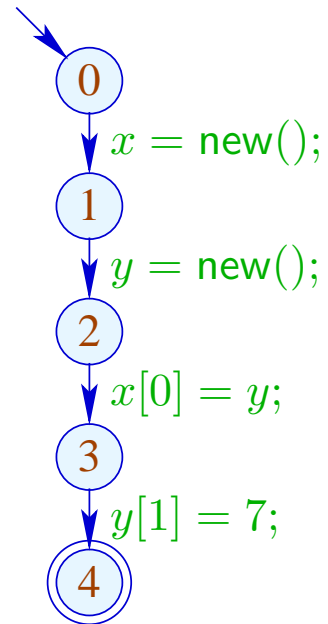
## Simple Example:

$x = \text{new}();$

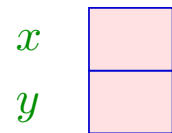
$y = \text{new}();$

$x[0] = y;$

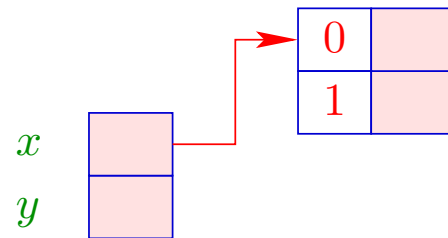
$y[1] = 7;$



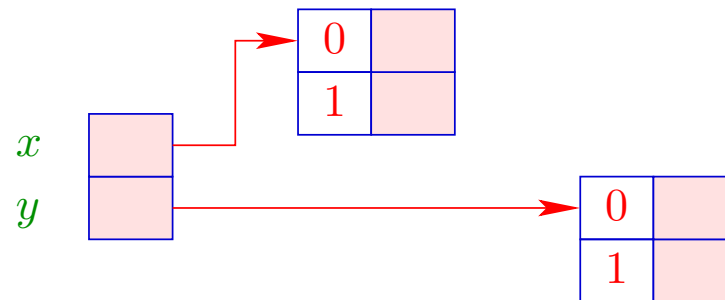
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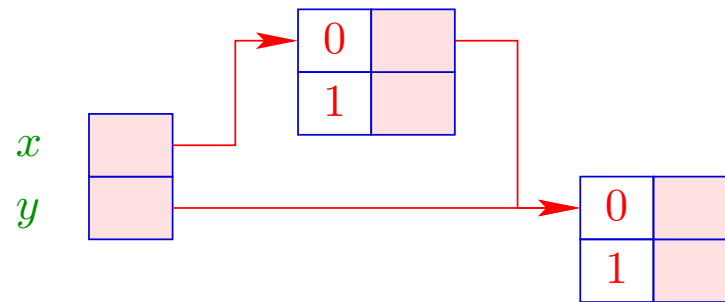


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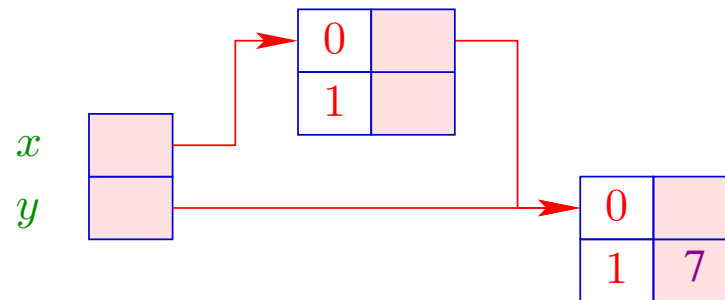




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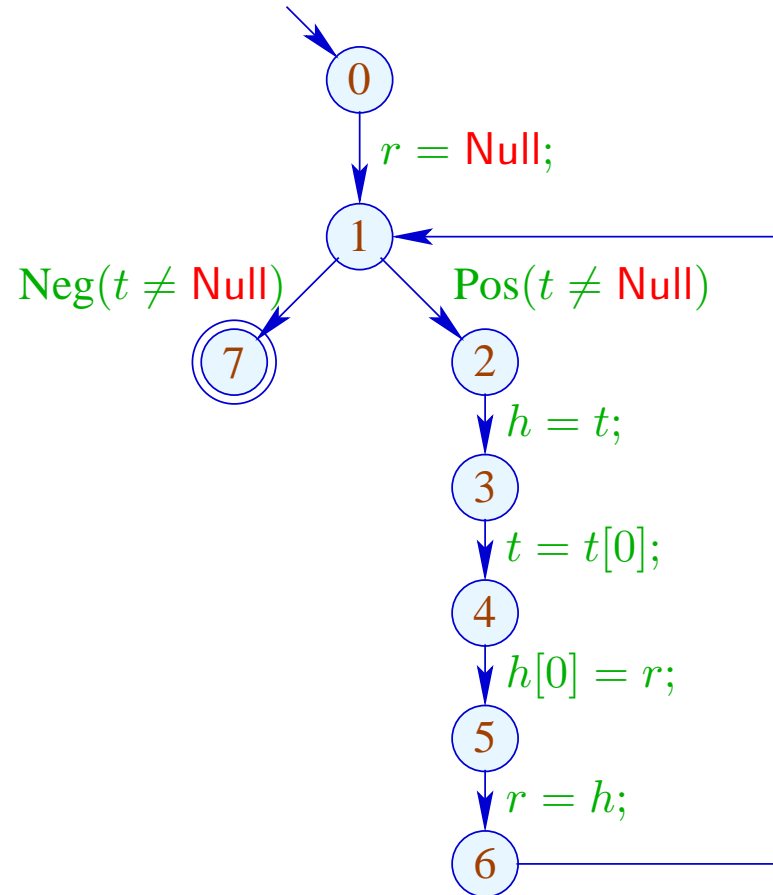


## The Semantics:



## More Complex Example:

```
r = Null;  
while (t ≠ Null) {  
    h = t;  
    t = t[0];  
    h[0] = r;  
    r = h;  
}
```



## Concrete Semantics:

A store consists of a **finite** collection of blocks.

After  $h$  new-operations we obtain:

$$Addr_h = \{\text{ref } a \mid 0 \leq a < h\} \quad // \text{ addresses}$$

$$Val_h = Addr_h \cup \mathbb{Z} \quad // \text{ values}$$

$$Store_h = (Addr_h \times \mathbb{N}_0) \rightarrow Val_h \quad // \text{ store}$$

$$State_h = (Vars \rightarrow Val_h) \times Store_h \quad // \text{ states}$$

For simplicity, we set:  $0 = \text{Null}$

Let  $(\rho, \mu) \in State_h$ . Then we obtain for the new edges:

$$\begin{aligned} \llbracket x = \text{new}(); \rrbracket (\rho, \mu) &= (\rho \oplus \{x \mapsto \text{ref } h\}, \\ &\quad \mu \oplus \{(\text{ref } h, i) \mapsto 0 \mid i \in \mathbb{N}_0\}) \\ \llbracket x = y[e]; \rrbracket (\rho, \mu) &= (\rho \oplus \{x \mapsto \mu(\rho y, \llbracket e \rrbracket \rho)\}, \mu) \\ \llbracket y[e_1] = e_2; \rrbracket (\rho, \mu) &= (\rho, \mu \oplus \{(\rho y, \llbracket e_1 \rrbracket \rho) \mapsto \llbracket e_2 \rrbracket \rho\}) \end{aligned}$$

## Caveat:

This semantics is **too** detailed in that it computes with **absolute** Addresses. Accordingly, the two programs:

$x = \text{new}();$	$y = \text{new}();$
$y = \text{new}();$	$x = \text{new}();$

are **not** considered as equivalent **!!?**

## Possible Solution:

Define equivalence only **up to permutation of addresses** :-)

# Alias Analysis

## 1. Idea:

- Distinguish **finitely many** classes of blocks.
- Collect all addresses of a block into one set!
- Use sets of addresses as abstract values!

⇒ **Points-to-Analysis**

$Addr^\# = Edges$  // creation edges

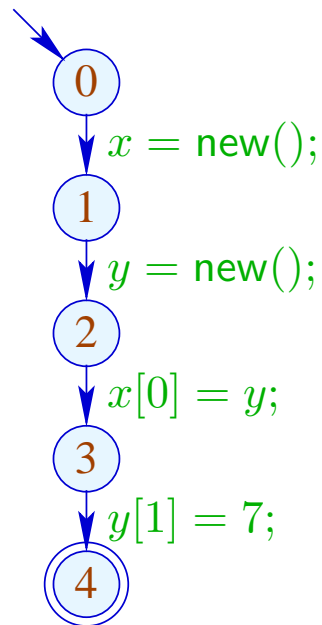
$Val^\# = 2^{Addr^\#}$  // abstract values

$Store^\# = Addr^\# \rightarrow Val^\#$  // abstract store

$State^\# = (Vars \rightarrow Val^\#) \times Store^\#$  // abstract states

// complete lattice !!!

... in the Simple Example:



	$x$	$y$	$(0, 1)$
0	$\emptyset$	$\emptyset$	$\emptyset$
1	$\{(0, 1)\}$	$\emptyset$	$\emptyset$
2	$\{(0, 1)\}$	$\{(1, 2)\}$	$\emptyset$
3	$\{(0, 1)\}$	$\{(1, 2)\}$	$\{(1, 2)\}$
4	$\{(0, 1)\}$	$\{(1, 2)\}$	$\{(1, 2)\}$



## The Effects of Edges:

$$\llbracket (\_, ;, \_) \rrbracket^\# (D, M) = (D, M)$$

$$\llbracket (\_, \text{Pos}(e), \_) \rrbracket^\# (D, M) = (D, M)$$

$$\llbracket (\_, x = y; , \_) \rrbracket^\# (D, M) = (D \oplus \{x \mapsto D y\}, M)$$

$$\llbracket (\_, x = e; , \_) \rrbracket^\# (D, M) = (D \oplus \{x \mapsto \emptyset\}, M) \quad , \quad e \notin \text{Vars}$$

$$\llbracket (u, x = \text{new}(); , v) \rrbracket^\# (D, M) = (D \oplus \{x \mapsto \{(u, v)\}\}, M)$$

$$\llbracket (\_, x = y[e]; , \_) \rrbracket^\# (D, M) = (D \oplus \{x \mapsto \bigcup \{M(f) \mid f \in D y\}\}, M)$$

$$\llbracket (\_, y[e_1] = x; , \_) \rrbracket^\# (D, M) = (D, M \oplus \{f \mapsto (M f \cup D x) \mid f \in D y\})$$

## Caveat:

- The value **Null** has been ignored. Dereferencing of **Null** or negative indices are not detected :-(  
- **Destructive updates** are only possible for variables, not for blocks in storage!

⇒ no information, if not all block entries are initialized before use :-((

- The effects now depend on the edge itself.

The analysis cannot be proven correct w.r.t. the reference semantics :-((

In order to prove correctness, we first **instrument** the concrete semantics with extra information which records where a block has been created.

...

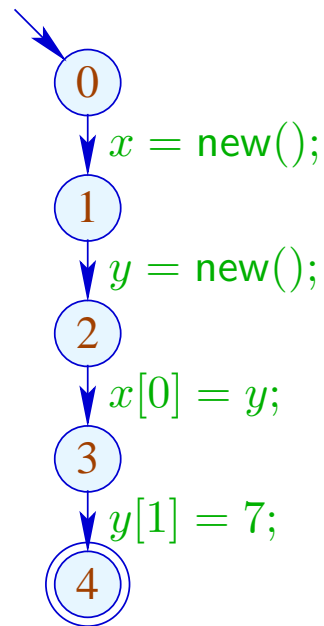
- We compute **possible** points-to information.
- From that, we can extract **may-alias** information.
- The analysis can be rather expensive — without finding very much  
:-(  
:-(
- Separate information for each program point can perhaps be abandoned ??

# Alias Analysis

## 2. Idea:

Compute for each variable and address a value which safely approximates the values at every program point simultaneously !

... in the Simple Example:



$x$	$\{(0, 1)\}$
$y$	$\{(1, 2)\}$
$(0, 1)$	$\{(1, 2)\}$
$(1, 2)$	$\emptyset$

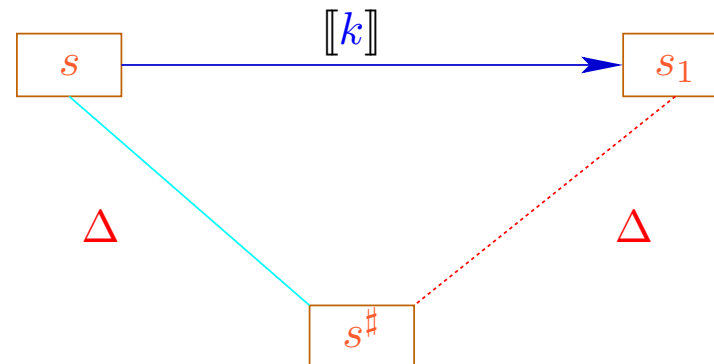
Each edge  $(u, lab, v)$  gives rise to constraints:

<i>lab</i>	<i>Constraint</i>
$x = y;$	$\mathcal{P}[x] \supseteq \mathcal{P}[y]$
$x = \text{new}();$	$\mathcal{P}[x] \supseteq \{(u, v)\}$
$x = y[e];$	$\mathcal{P}[x] \supseteq \bigcup \{\mathcal{P}[f] \mid f \in \mathcal{P}[y]\}$
$y[e_1] = x;$	$\mathcal{P}[f] \supseteq (f \in \mathcal{P}[y]) ? \mathcal{P}[x] : \emptyset$ for all $f \in Addr^\#$

Other edges have no effect :-)

## Discussion:

- The resulting constraint system has size  $\mathcal{O}(k \cdot n)$  for  $k$  abstract addresses and  $n$  edges :-)
- The number of necessary iterations is  $\mathcal{O}(k(k + \#Vars))$  ...
- The computed information is perhaps still too **zu precise !!!**
- In order to prove correctness of a solution  $s^\# \in States^\#$  we show:

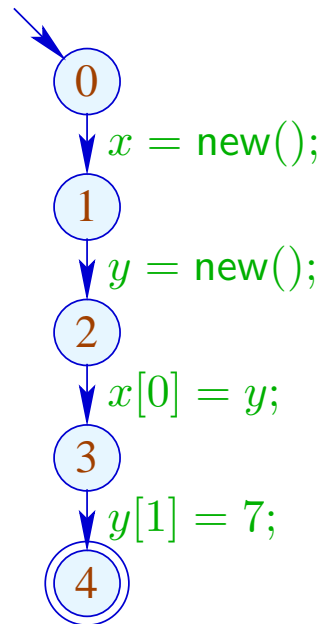


# Alias Analysis

## 3. Idea:

Determine **one** equivalence relation  $\equiv$  on variables  $x$  and memory accesses  $y[]$  with  $s_1 \equiv s_2$  whenever  $s_1, s_2$  may contain the same address at **some**  $u_1, u_2$

... in the Simple Example:



$$\equiv = \{ \{x\}, \{y, x[]\}, \{y[]\} \}$$

## Discussion:

- We compute a **single information** fo the whole program.
- The computation of this information maintains **partitions**  
 $\pi = \{P_1, \dots, P_m\}$  :-)
- Individual sets  $P_i$  are identified by means of **representatives**  
 $p_i \in P_i$ .
- The operations on a partition  $\pi$  are:

$$\begin{aligned} \text{find}(\pi, p) &= p_i && \text{if } p \in P_i \\ & // && \text{returns the representative} \\ \text{union}(\pi, p_{i_1}, p_{i_2}) &= \{P_{i_1} \cup P_{i_2}\} \cup \{P_j \mid i_1 \neq j \neq i_2\} \\ & // && \text{unions the represented classes} \end{aligned}$$



- If  $x_1, x_2 \in Vars$  are equivalent, then also  $x_1[]$  and  $x_2[]$  must be equivalent :-)
- If  $P_i \cap Vars \neq \emptyset$ , then we choose  $p_i \in Vars$ . Then we can apply **union** recursively :

```

union* ( $\pi, q_1, q_2$ ) = let  $p_{i_1} = \mathbf{find}(\pi, q_1)$ 
                              $p_{i_2} = \mathbf{find}(\pi, q_2)$ 
in if  $p_{i_1} == p_{i_2}$  then  $\pi$ 
   else let  $\pi = \mathbf{union}(\pi, p_{i_1}, p_{i_2})$ 
in if  $p_{i_1}, p_{i_2} \in Vars$  then
    union* ( $\pi, p_{i_1}[], p_{i_2}[]$ )

```